

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Timothy A. Rost	Docket No.:	TI-36595
Serial No.:	10/785,648	Art Unit:	2814
Filing Date:	February 24, 2004	Examiner:	Pham, Long
Customer No.:	23494	Conf. No.:	3709
Title:	Transistor Design and Layout for Performance Improvement with Strain		

**AMENDMENT UNDER 37 CFR 1.111**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated January 11, 2008. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

There are no **Amendments to the Specification**.

**Amendments to the Claims** are reflected in the listing of claims that begin on page 2 of this paper.

There are no **Amendments to the Drawings**.

The **Remarks** begin on page 5.